VCXO STANDARD CERAMIC SMD (7.0 x 5.0 x 2.0mm)

**FEATURES:**
- Low Noise
- Wide Frequency Range up to 200.000MHz

<table>
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<tr>
<th>SPECIFICATION</th>
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<tr>
<td>PARAMETER</td>
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<tr>
<td>FREQUENCY RANGE</td>
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<tr>
<td>FREQUENCY STABILITY</td>
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<tr>
<td>OPERATING TEMPERATURE RANGE</td>
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<tr>
<td>STORAGE TEMPERATURE RANGE</td>
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<tr>
<td>SUPPLY VOLTAGE ±10%</td>
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<td>SUPPLY CURRENT</td>
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<th>OUTPUT</th>
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<tbody>
<tr>
<td>LOGIC FAMILY</td>
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<tr>
<td>LOAD</td>
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<tr>
<td>LEVEL LOGIC ‘1’ (V_{OH})</td>
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<tr>
<td>LEVEL LOGIC ‘0’ (V_{OL})</td>
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<tr>
<td>SYMMETRY (DUTY CYCLE)</td>
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<td>RISE AND FALL TIME (Tr/Tf)</td>
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<td>START-UP TIME</td>
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<th>STAND-BY VOLTAGE</th>
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<tr>
<td>ENABLE (V_{EN})</td>
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<tr>
<td>DISABLE (V_{DL})</td>
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</table>

| ENABLE/ DISABLE DELAY TIME | 100 ns |
| FREQUENCY DEVIATION | ±50 ppm |
| CONTROL VOLTAGE | 0 V to 1/2 V_{DD} |
| SLOPE | Positive |
| LINEARITY | 10 % |
| MODULATION BANDWIDTH @-3dB | 10 kHz |
| INPUT IMPEDANCE | 50 kΩ |
| ABSOLUTE PULL RANGE (Optional) | ±50 ppm |
| AGING @ 25°C + 3°C | ±3 ppm |

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<tr>
<th>PHASE JITTER RMS</th>
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<tr>
<td>FUNDAMENTAL PLL (3.3V)</td>
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<td>PLL (3.3V)</td>
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</tbody>
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**PACKAGE DIMENSIONS**

**SOLDER PATTERN**

**REFLOW PROFILE**

**PART NUMBERING GUIDE**

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