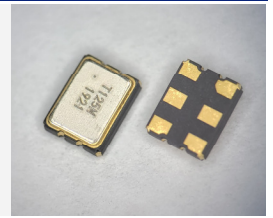


## CERAMIC PECL MINIATURE SMD OSCILLATOR (3.2 x 2.5 x 1.2mm)

### FEATURES:

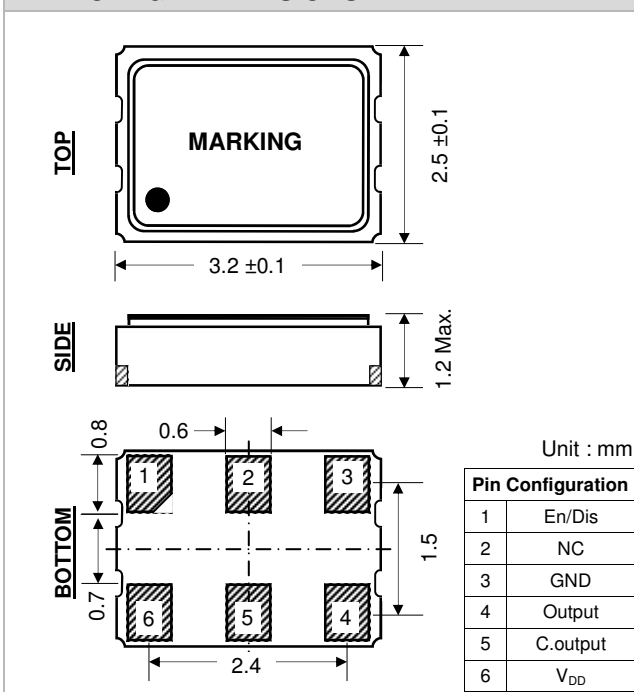
- WIDE FREQUENCY RANGE
- LOW JITTER
- EN/DISABLE PIN AVAILABLE



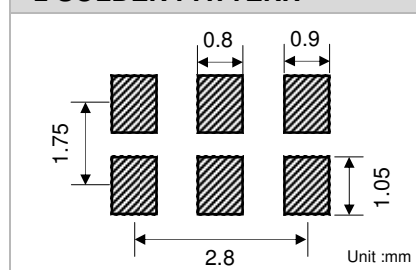
### ■ SPECIFICATION

<b>FREQUENCY RANGE</b>	13.500MHz ~ 156.250MHz	
<b>FREQUENCY STABILITY</b>	STD: $\pm 50$ ppm, $\pm 10$ ppm ~ $\pm 100$ ppm max. (See P/N guide for other options)	
<b>OPERATING TEMPERATURE RANGE</b>	STD: $0^{\circ}\text{C}$ ~ $70^{\circ}\text{C}$ , option: $-40^{\circ}\text{C}$ ~ $85^{\circ}\text{C}$ (See P/N guide for other options)	
<b>STORAGE TEMPERATURE RANGE</b>	$-55^{\circ}\text{C}$ ~ $125^{\circ}\text{C}$	
<b>SUPPLY VOLTAGE <math>\pm 10\%</math></b>	$2.5V_{\text{DC}}$ , $3.3V_{\text{DC}}$	
<b>SUPPLY CURRENT</b>	50mA max.	
<b>OUTPUT</b>	<b>LOGIC FAMILY</b>	LVPECL
	<b>LOAD</b>	$50\Omega$ (into $V_{\text{DD}} - 2.0V_{\text{DC}}$ )
	<b>LEVEL LOGIC '1' (<math>V_{\text{OH}}</math>)</b>	$V_{\text{DD}} - 1.025V_{\text{DC}}$ min.
	<b>LEVEL LOGIC '0' (<math>V_{\text{OL}}</math>)</b>	$V_{\text{DD}} - 1.620V_{\text{DC}}$ max.
	<b>PEAK TO PEAK</b>	600mV min. 800mV typ.
	<b>SYMMETRY(DUTY CYCLE)</b>	STD: 40/60%, Option: 45/55% (See P/N guide for other options)
	<b>RISE AND FALL TIME (<math>T_{\text{r}}/T_{\text{f}}</math>)</b>	250pS typ. 1nS max.
	<b>START UP TIME</b>	5mS max.
<b>START-UP TIME</b>	5mS typ. 10mS max.	
<b>STAND-BY VOLTAGE</b>	<b>ENABLE (<math>V_{\text{IH}}</math>)</b>	$0.7 \times V_{\text{DD}} \leq V_{\text{IH}} \leq V_{\text{DD}}$ or N/C
	<b>DISABLE (<math>V_{\text{IL}}</math>)</b>	$V_{\text{IL}} \leq 0.3 \times V_{\text{DD}}$
<b>STAND-BY CURRENT (Pin#1=<math>V_{\text{IL}}</math>)</b>	10 $\mu\text{A}$ max.	
<b>ENABLE DELAY TIME</b>	10mS max.	
<b>DISABLE DELAY TIME</b>	200nS max.	
<b>AGING @ <math>+25^{\circ}\text{C}</math> <math>\pm 3^{\circ}\text{C}</math></b>	$\pm 3.0$ ppm / 1 <sup>st</sup> year max.	
<b>PHASE JITTER RMS (12kHz to 20MHz)</b>	1.0pS max.	

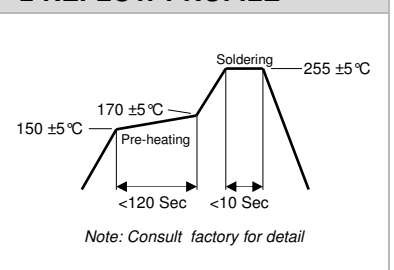
### ■ PACKAGE DIMENSIONS



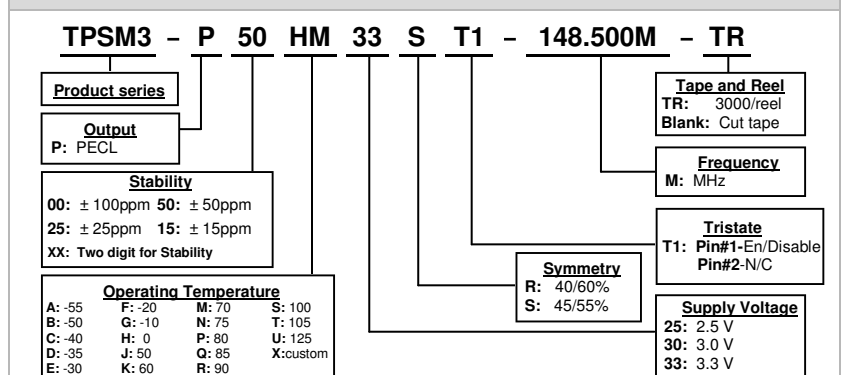
### ■ SOLDER PATTERN



### ■ REFLOW PROFILE



### ■ PART NUMBERING GUIDE



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