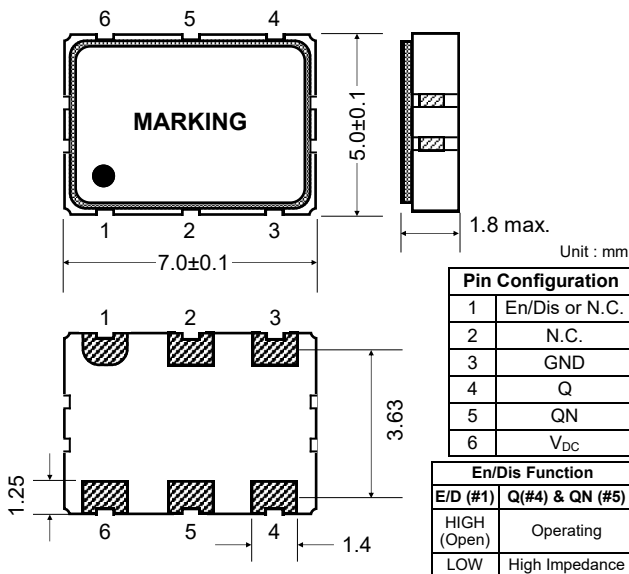
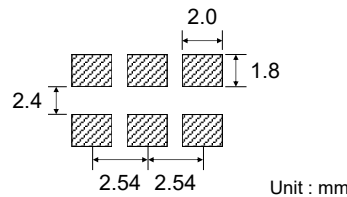
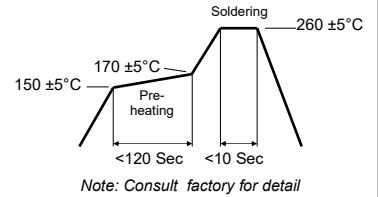
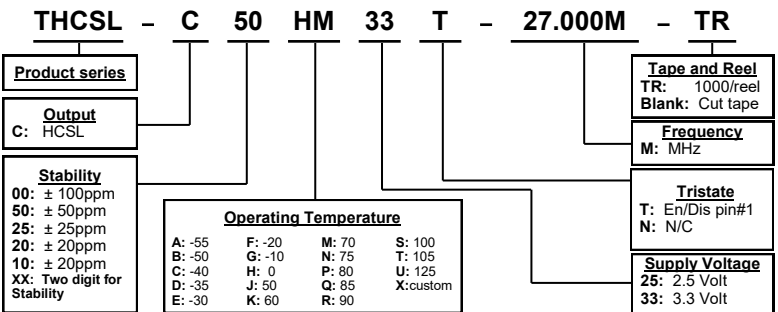


CERAMIC SMD HCSL OUTPUT OSCILLATOR (7.0 x 5.0 x 1.8mm)
FEATURES:

- LOW PHASE JITTER (12kHz TO 20MHz) UNDER 1pS (RMS)
- RoHS COMPLIANT
- LOW PROFILE PACKAGE DESIGN (1.8MM HEIGHT MAX.)


■ SPECIFICATION

FREQUENCY RANGE		13.500MHz ~ 200.000MHz	
FREQUENCY STABILITY		±25ppm ~ ±100ppm	
OPERATING TEMPERATURE RANGE		STD: -10°C ~ 70°C, Option: -40°C ~ 85°C (See P/N guide for other options)	
STORAGE TEMPERATURE RANGE		-55°C ~ 125°C	
SUPPLY VOLTAGE ± 5%		2.5V _{DC}	3.3V _{DC}
SUPPLY CURRENT	f < 90MHz	17mA typ. 27mA max.	
	90MHz ≤ f < 125MHz	18mA typ. 28mA max.	
	125MHz ≤ f < 160MHz	19mA typ. 29mA max.	
	160MHz ≤ f	30mA typ. 50mA max.	
OUTPUT	LOGIC FAMILY	HCSL square wave	
	LOAD	50Ω (Between output and GND)	
	LEVEL LOGIC '1' (V_{OH})	0.55V ≤ V _{OH} ≤ 0.8V	0.6V ≤ V _{OH} ≤ 0.85V
	LEVEL LOGIC '0' (V_{OL})	-0.16V ≤ V _{OL} ≤ 0.15V	-0.16V ≤ V _{OL} ≤ 0.15V
	PEAK TO PEAK @ V_{DD}= 2.5V	0.6V typ.	0.65V typ.
	SYMMETRY(DUTY CYCLE)	45% / 55%	
	RISE AND FALL TIME (Tr/Tf)	0.15nS typ. 0.7nS max.	
	START-UP TIME	3mS typ. 10mS max.	
STAND-BY VOLTAGE	ENABLE (V_{IH})	2.4V ≤ V _{IH} ≤ V _{DC} or N.C.	
	DISABLE (V_{IL})	V _{IL} ≤ 0.6V	
STAND-BY CURRENT		10μA	
AGING @ 25°C ± 3°C		±3.0ppm / 1 st year max.	
PHASE JITTER RMS (12KHZ ~ 20MHz)		200fS typ.	
PHASE NOISE @ (125MHz OFFSET)		-50dBc/Hz @ 10Hz -82dBc/Hz @ 100Hz -116dBc/Hz @ 1kHz -138dBc/Hz @ 10kHz	-144dBc/Hz @ 100kHz -149dBc/Hz @ 1MHz -155dBc/Hz @ 10MHz

■ PACKAGE DIMENSIONS

■ SOLDER PATTERN

■ REFLOW PROFILE

■ PART NUMBERING GUIDE


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