

## PECL OUTPUT THROUGH HOLE OSCILLATOR (20.8 x 13.2 x 7.24mm)

### FEATURES:

- WIDE FREQUENCY RANGES
- COMPLEMENTARY OUTPUT
- STANDARD PACKAGE

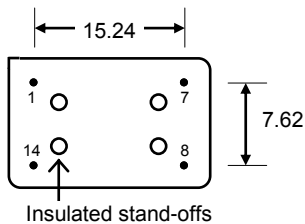
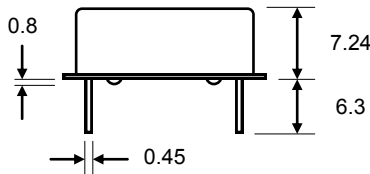
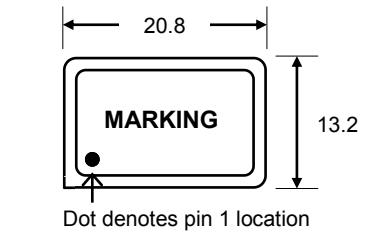


### ■ SPECIFICATION

FREQUENCY RANGE		0.75MHz ~ 800.0MHz
FREQUENCY STABILITY		±10ppm ~ ±100ppm
OPERATING TEMPERATURE RANGE		STD: 0°C ~ 70°C (See P/N guide for other options)
STORAGE TEMPERATURE RANGE		-55°C ~ 105°C
SUPPLY VOLTAGE ±5%		3.3V <sub>DC</sub> , 5.0V <sub>DC</sub>
SUPPLY CURRENT	24 MHz ~ 800 MHz	25mA max. ~ 100mA max.
OUTPUT	LOGIC FAMILY	PECL
	LOAD	50Ω (into V <sub>DD</sub> - 2.0V <sub>DC</sub> )
	LEVEL LOGIC '1' (V <sub>OH</sub> )	V <sub>DD</sub> - 1.025V <sub>DC</sub> min.
	LEVEL LOGIC '0' (V <sub>OL</sub> )	V <sub>DD</sub> - 1.620V <sub>DC</sub> max.
	SYMMETRY (DUTY CYCLE)	45% / 55%
STAND-BY VOLTAGE *	RISE AND FALL TIME (Tr/Tf)	1.0nS max.
	ENABLE (V <sub>IL</sub> )	0.8V <sub>DC</sub> max.
	DISABLE (V <sub>IH</sub> )	2.0V <sub>DC</sub> min. or N/C
AGING @ 25°C ± 3°C		±3.0ppm / 1 <sup>st</sup> year max.
PHASE JITTER RMS (12kHz ~ 20MHz)		1.0pS max. (Fundamental), 3pS max. (PLL)

\* Disable (V<sub>IL</sub>) / Enable (V<sub>IH</sub>) is also available.

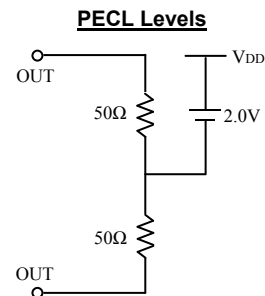
### ■ PACKAGE DIMENSIONS



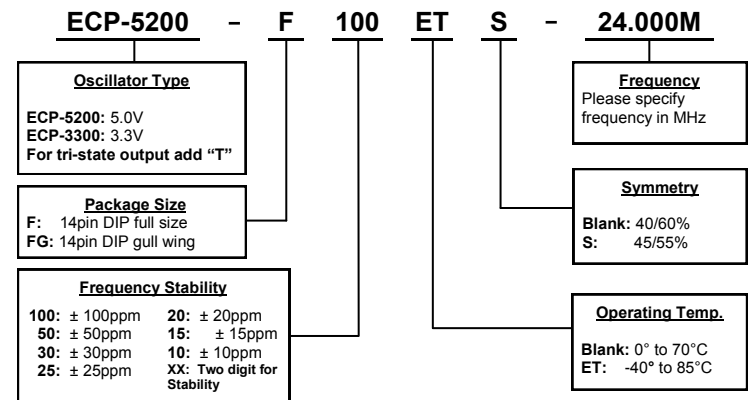
Unit : mm

Pin Configuration	
1	C.Output / En/Dis / N.C.
7	GND
8	OUTPUT
14	V <sub>DD</sub>

### ■ TEST CIRCUIT



### ■ PART NUMBERING GUIDE



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