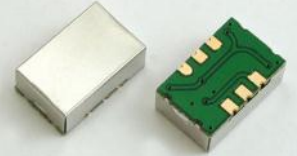


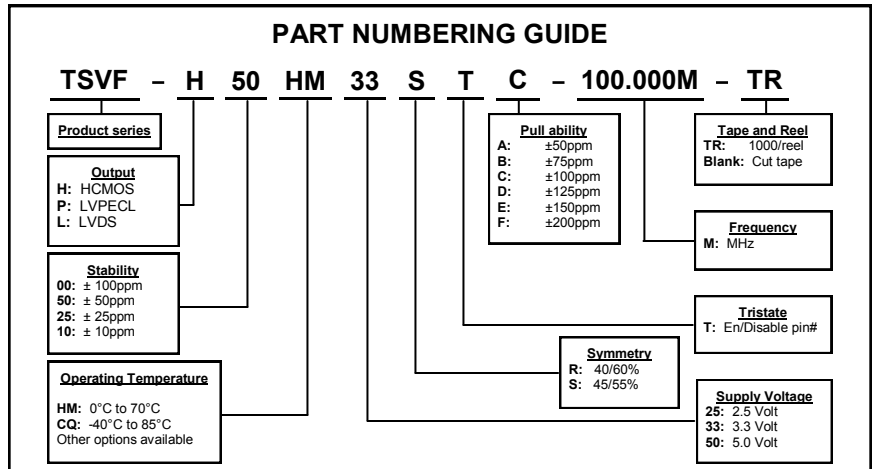
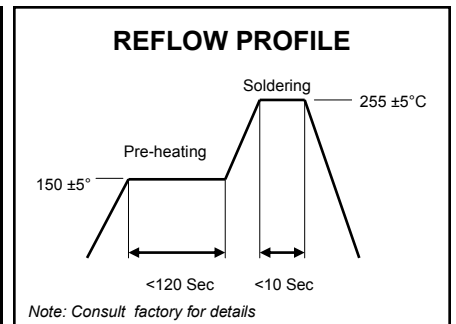
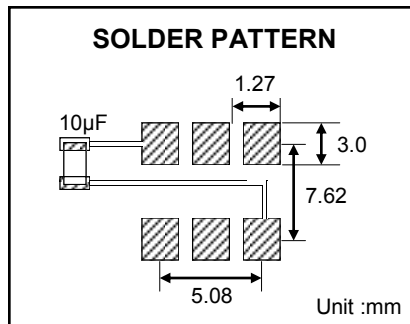
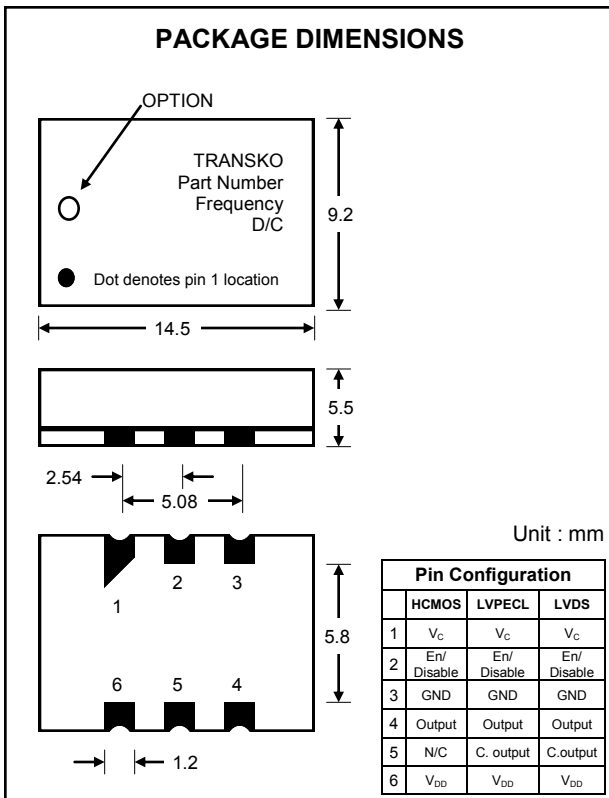
FR4 PCB BASED SMD LVPECL/LVDS/CMOS VCXO (14.5 x 9.2 x 5.5mm)

FEATURES:

- HCMOS/LVPECL/LVDS HIGH FREQUENCY RANGE
- LOW JITTER / LOW CURRENT
- SUITABLE FOR REFLOW

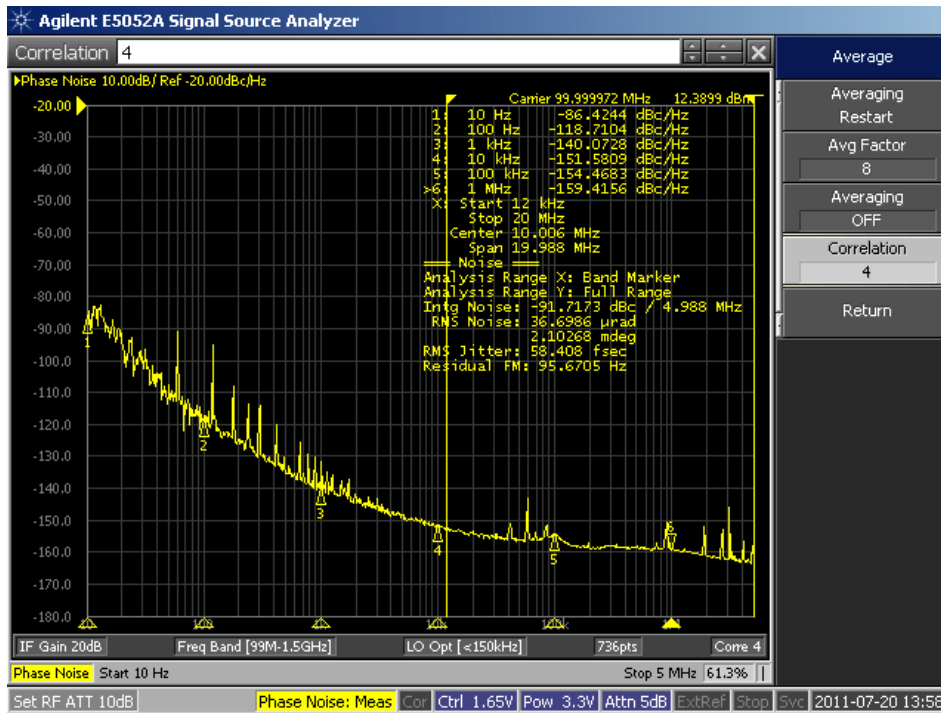


MODEL	TSVF		
OUTPUT	HCMOS	LVPECL	LVDS
FREQUENCY RANGE	1.000MHz ~ 800.000MHz		
INPUT VOLTAGE	STD: 3.3V ± 5%, Option: 5.0V ± 5%		
TEMPERATURE STABILITY	±10ppm ~ ±100ppm max.		
OPERATING TEMPERATURE	STD: 0°C to 70°C, Option: -40°C to 85°C		
INPUT CURRENT	10mA ~ 100mA max	15mA ~ 80mA max	15mA ~ 65mA max
OUTPUT LOGIC '1' LEVEL (Voh)	90% min of V _{DD}	V _{DD} - 1.02V	1.4V Typical, 1.6V max
OUTPUT LOGIC '0' LEVEL (Vol)	10% max of V _{DD}	V _{DD} - 1.63V	1.1V Typical, 0.9V min.
SYMMETRY	40/60%	45/55%	40/60%
RISE & FALL TIME	1.0nS ~ 5.0nS max	1.0nS max	1.0nS max
PULLABILITY	± 50ppm ~ ±200ppm (Please contact factory for wider range)		
CONTROL VOLTAGE	1.65V ± 1.5V (3.3V supply), 2.5V ± 2.5V (5.0V supply)		
LINEARITY	10% typical, 20% max		
OUTPUT LOAD	15pF typical	50Ω	100Ω
INPUT IMPEDANCE	100KΩ min.		
PHASE JITTER	0.5 ~ 2.0pS typical depending on frequency		
PHASE NOISE	-154.4683 dBc/Hz @ 100.000MHz		
AGING	± 3.0ppm / year max.		

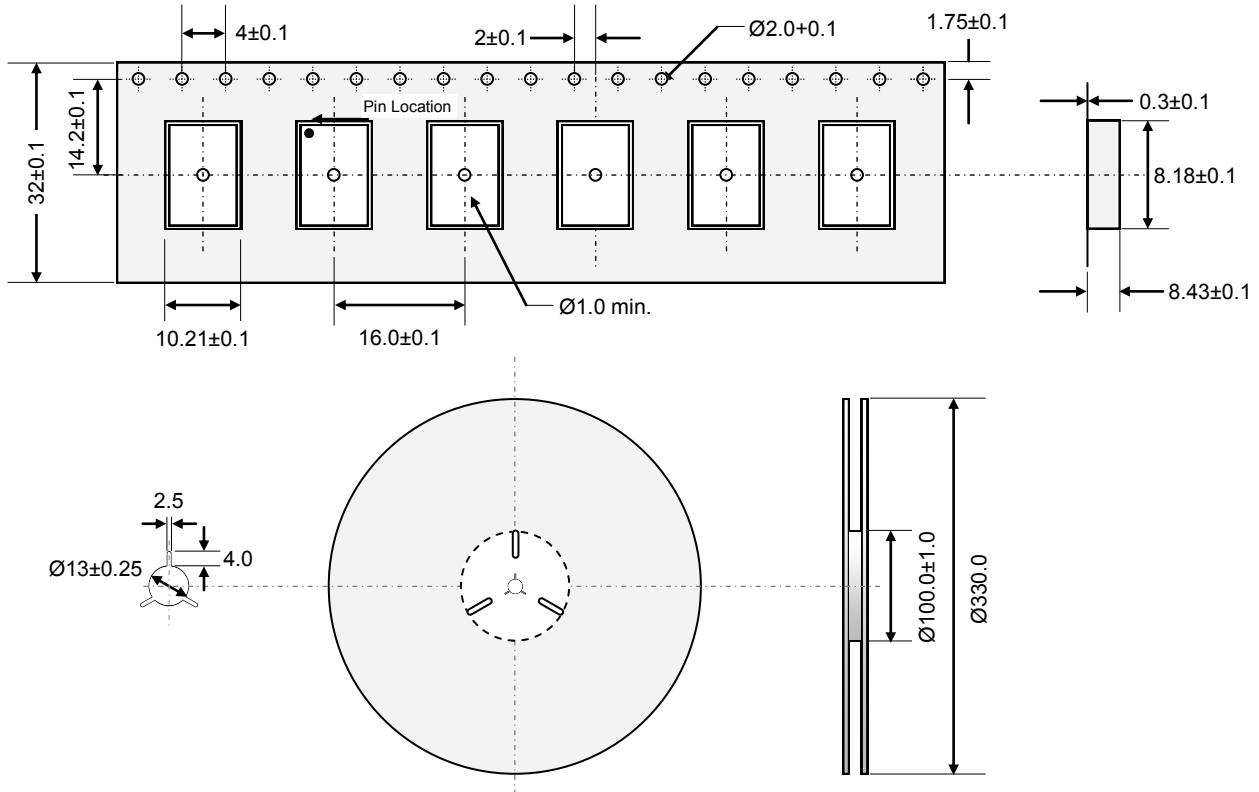


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PHASE NOISE



TAPE & REEL DIMENSIONS



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