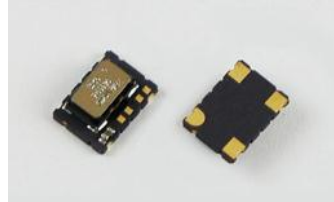


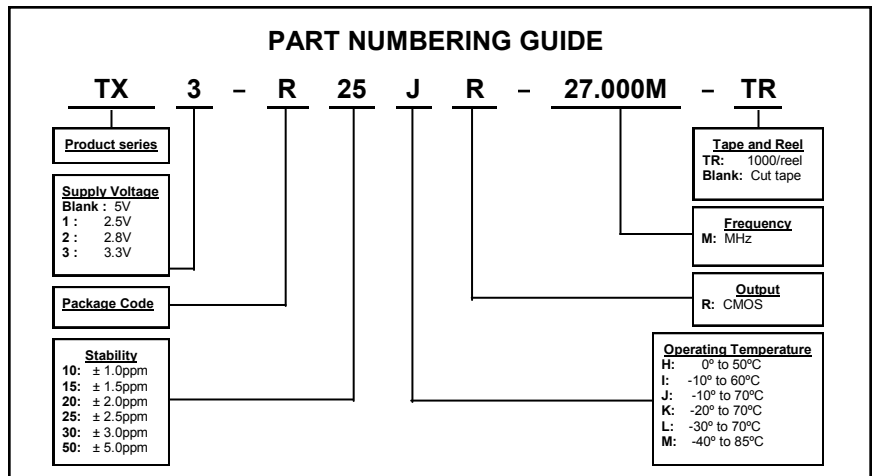
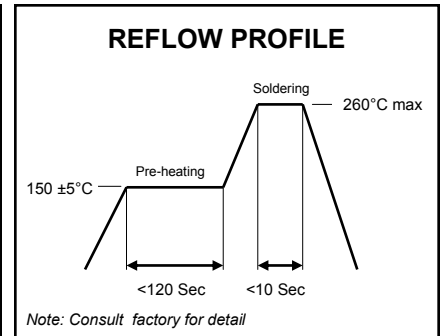
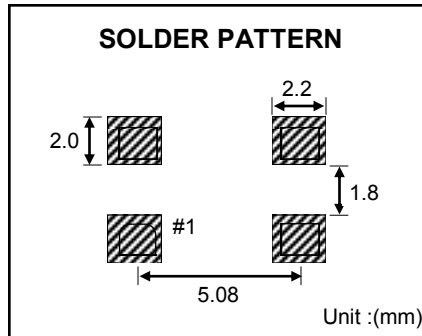
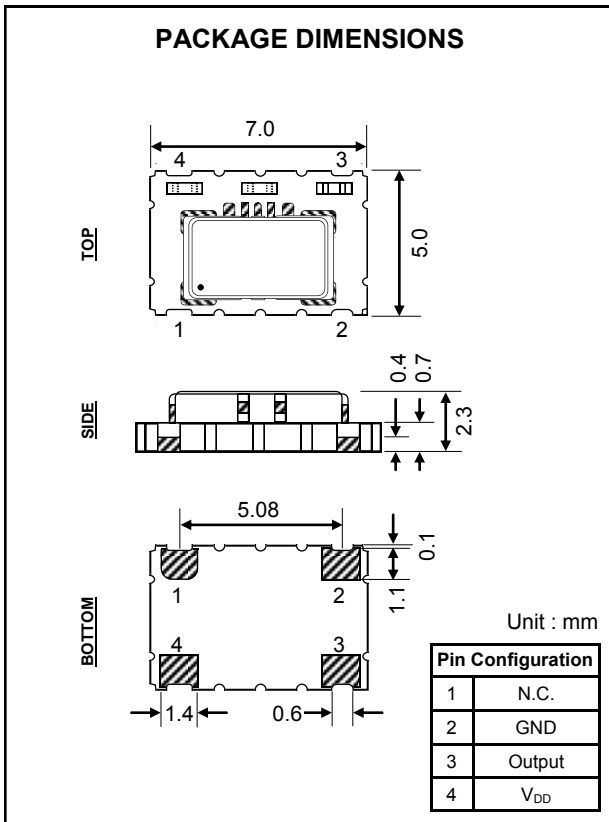
## TCXO SMD (7.0 x 5.0 x 2.3mm)

### FEATURES:

- HIGH FREQUENCY UPTO 170.000MHz
- CMOS OUTPUT
- TIGHT STABILITY  $\pm 1.0\text{ppm}$  vs.  $-40^{\circ}\text{C}$  TO  $85^{\circ}\text{C}$



SUPPLY VOLTAGE	2.8V	3.0 ~ 3.3V	5.0V
FREQUENCY RANGE	2.500000MHz ~ 170.000000MHz		
STANDARD FREQUENCIES	10.0, 12.8, 13.0, 14.4, 16.0, 16.384, 19.2, 19.440, 19.680, 20.0, 38.880, 77.760, 155.520 MHz		
FREQUENCY TOLERANCE	$\pm 2.0\text{ppm}$ @ $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , $V_c = +1.5V_{DC}$		
OUTPUT WAVE FORM	CMOS Square wave		
FREQUENCY STABILITY	Reference from $25^{\circ}\text{C}$		
vs. OPERATING TEMPERATURE	$\pm 0.5\text{ppm} \sim \pm 2.5\text{ppm}$		
vs. SUPPLY VOLTAGE $\pm 5\%$	$\pm 0.3\text{ppm}$ max.		
vs. LOAD $\pm 5\%$	$\pm 0.3\text{ppm}$ max.		
vs. AGING	$\pm 1.0\text{ppm}$ max. @ $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$		
vs. REFLOW	$\pm 1.0\text{ppm}$ max. 1 reflow and measured 24 hours afterwards		
SUPPLY CURRENT	2mA ~ 16mA max	2 ~ 21mA max	5mA ~ 43mA max
OUTPUT LOGIC '1' LEVEL (Voh)	2.4V min.		
OUTPUT LOGIC '0' LEVEL (Vol)	0.4V max.		
RISE AND FALL TIME	10nS max 20% ~ 80% waveform		
DUTY CYCLE	50% $\pm 10\%$ measured at 50% $V_{DD}$		
START UP TIME	10mS max.		
PHASE NOISE	Tested at 10MHz: 10Hz: -72dBc/Hz, 100Hz: -110dBc/Hz, 1KHz: -125dBc/Hz, 10KHz: -132dBc/Hz typical.		



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