

CERAMIC SMD LVDS OUTPUT OSCILLATOR (7.0 x 5.0 x 1.8mm)

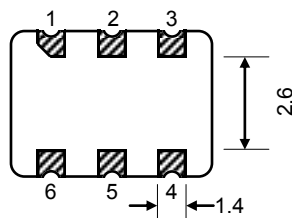
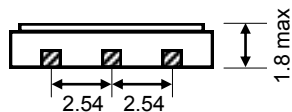
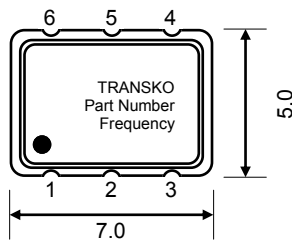
FEATURES:

- HIGH FREQUENCY
- ULTRA LOW PHASE JITTER PERFORMANCE
- ENABLE / DISABLE



MODEL	TLSM	
SUPPLY VOLTAGE	2.5V	3.3V
FREQUENCY RANGE	12.000MHz ~ 312.500MHz	12.000MHz ~ 800.000MHz
FREQUENCY STABILITY	±15ppm ~ ±100ppm	
OPERATING TEMPERATURE	0°C to 70°C, Option: -40°C to 85°C	
STORAGE TEMPERATURE	-55°C to 125°C	
INPUT CURRENT	100mA max.	
DUTY CYCLE	50% ±5% (50% of waveform)	
RISE TIME / FALL TIME	250pS typical, 600pS max.	
OUTPUT LOGIC '1' LEVEL (Voh)	1.4V Typical, 1.6V max.	
OUTPUT LOGIC '0' LEVEL (Vol)	1.1V Typical, 0.9V min.	
OUTPUT PEAK TO PEAK SWING	250mV min. 350mV typical, 460mV max.	
OUTPUT OFFSET (RL = 100Ω)	1.125V min., 1.25V typical, 1.375V max.	
LVDS OUTPUT LOAD	100 Ω	
AGING @ 25°C	± 3.0ppm max.	
PHASE JITTER (12KHz ~ 20MHz offset)	12.000MHz ~ 200.000MHz: 1.0pS max. 201.000MHz ~ 800.000MHz: 3.0pS max.	
TRISTATE OPTION	T1: Pin# 1 En/Dis Pin#2: N/C, T2: Pin#1 N/C Pin# 2: En/Dis	

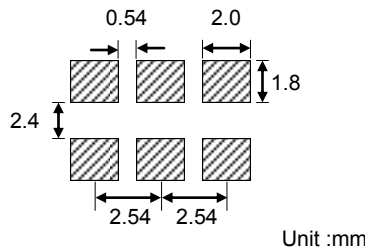
PACKAGE DIMENSIONS



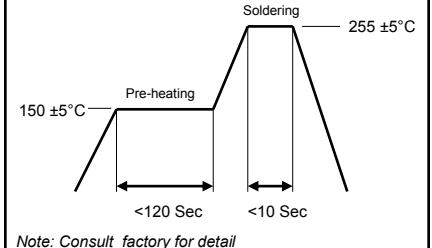
Unit : mm

Pin Configuration	
1	N.C. or En/Dis
2	N.C. or En/Dis
3	GND
4	Output
5	C - output
6	V _{DD}

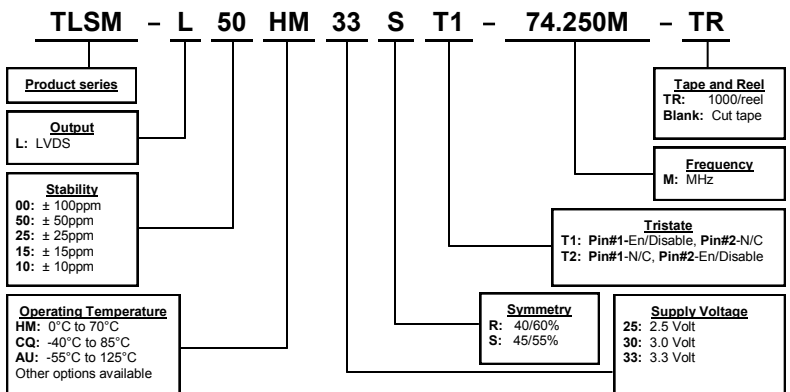
SOLDER PATTERN



REFLOW PROFILE



PART NUMBERING GUIDE



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